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2	18	((skin adj effect) and (model or simulat\$4)) and (s adj2 parameter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/06 08:43
3	1		USPAT	2004/09/06 08:39
4	2	((skin adj effect) and (model or simulat\$4)) and (strassen\$2 adj method)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/06 08:44
	4	(dielectric adj2 loss\$2) same (transmission adj2 line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/05 10:40
	0	(dielectric adj2 loss\$2) same (transmission adj2 line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/05 10:40
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-	3386	(IC adj2 chip) and ((signal adj2 degradation) or (dielectric) or (transmission adj2 loss\$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/05 10:46
-	15	((IC adj2 chip) and ((signal adj2 degradation) or (dielectric) or (transmission adj2 loss\$2))) and spice	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/06 08:27



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1 **Breaking the ice [transmission line icing]**

Sullivan, C.R.; Petrenko, V.F.; McCurdy, J.D.; Kozliouk, V.;

Industry Applications Magazine, IEEE , Volume: 9 , Issue: 5 , Sept.-Oct. 2003

Pages:49 - 54

[\[Abstract\]](#) [\[PDF Full-Text \(4088 KB\)\]](#) **IEEE JNL**

2 **Time domain modeling of lossy interconnects**

Svensson, C.; Dermer, G.H.;

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Pages:191 - 196

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3 **Effectiveness analysis of lossy dielectric shields for a three-layered human model**

Nishizawa, S.; Hashimoto, O.;

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4 **Simple modeling of coplanar waveguide on thick dielectric over lossy substrate**

Jin-Su Ko; Bon-Kee Kim; Kwyro Lee;

Electron Devices, IEEE Transactions on , Volume: 44 , Issue: 5 , May 1997

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5 **Time-domain simulation of multiconductor transmission lines with**

frequency-dependent losses*Gordon, C.; Blazeck, T.; Mittra, R.;*

Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on , Volume: 11 , Issue: 11 , Nov. 1992
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6 Highly accurate quasi-static modeling of microstrip lines over lossy substrates*Tuncer, E.; Neikirk, D.P.;*

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9 Microwave education supported by animations of wave propagation effects*Menzel, W.;*

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10 Lossy power distribution networks with thin dielectric layers and/or thin conductive layers*Novak, I.;*

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11 High Q-factor inductors integrated on MCM Si substrates*Zu, L.; Yicheng Lu; Frye, R.C.; Lau, M.Y.; Chen, S.-C.S.; Kossives, D.P.; Jenshan Lin; Tai, K.L.;*

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16 Analysis of lossy multi-chip module interconnections using finite element method

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